

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3772	365/200.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 12:31
L2	3551	365/230.03.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 12:31
L3	2893	365/203.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 12:33
L4	912	365/230.05.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 12:39
L5	3264	365/201.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 12:41
L6	480	L1 and L5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 12:41
L7	125	(redundant adj3 (array bank block)) and L6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 12:42
L8	771	(redundant adj3 (array bank block)) and L1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 12:42

L9	16	L7 and (precharg\$3 pre-charg\$3) adj3 circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 12:45
L10	91	L8 and (precharg\$3 pre-charg\$3) adj3 circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 13:00
L11	7	L9 and (sram static)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 12:43
L12	42	L10 and (sram static)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 13:00
L13	2	(((regular normal) near3 (array block bank)) with (redundant near3 replacement near3 unit)) and ((input/output) (I/O)) and (redundant near3 (array block bank)) and (redundant near3 replacement near3 memory) and (precharg\$3 (pre adj charg\$3)). clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 12:48
L14	2	(((regular normal) near3 (array block bank)) with (redundant near3 replacement near3 unit)) and ((input/output) (I/O)) and (redundant near3 (array block bank)) and (redundant near3 replacement near3 memory) and (precharg\$3 (pre adj charg\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 12:50
L15	8	((data infomation) near3 stored near3 (redundant near3 (memory unit array block bank))) with (replac\$3 (not\$1 adj replac\$3)) with ((regular normal) near3 (memory unit array block bank))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 13:03
L16	1	L10 and L15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 13:03

S1	747	(redundant adj3 (array bank block)) and 365/200.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 12:41
S2	87	S1 and (precharg\$3 pre-charg\$3) adj3 circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 12:42
S3	41	S2 and (sram static)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 12:43